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NOTICE OF ALLOWANCE AND FEE(S) DUE

27799

7590

10/28/2010

COHEN, PONTANI, LIEBERMAN & PAVANE LLP 551 FIFTH AVENUE SUITE 1210 NEW YORK, NY 10176 EXAMINER

SONG, MATTHEW J

ART UNIT PAPER NUMBER

1714

DATE MAILED: 10/28/2010

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/566,521	09/25/2006	Volker Harle	5367-219PUS	7206

TITLE OF INVENTION: METHOD FOR THE PRODUCTION OF A PLURALITY OF OPTO-ELECTRONIC SEMICONDUCTOR CHIPS AND

OPTO-ELECTRONIC SEMICONDUCTOR CHIP

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	01/28/2011

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

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A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

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appropriate. All further indicated unless correcte maintenance fee notifical	correspondence includir ed below or directed oth	ng the Patent, advance on the nerwise in Block 1, by (a	rders and notification of a specifying a new corre	maintenance fees v spondence address	will be ; and/o	mailed to the current r (b) indicating a sepa	correspondence address as rate "FEE ADDRESS" for
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NEW YORK, N	Y 10176						(Depositor's name)
							(Signature)
			<u></u>				(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTC	RNEY DOCKET NO.	CONFIRMATION NO.
10/566,521	09/25/2006		Volker Harle		•	5367-219PUS	7206
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nonprovisional	NO	\$1510	\$300	\$0		\$1810	01/28/2011
EXAM	INER	ART UNIT	CLASS-SUBCLASS]			
SONG, MA	ATTHEW J	1714	117-089000				
 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. 			or agents OR, alternati (2) the name of a single registered attorney or	s of up to 3 registered patent attorneys, alternatively, of a single firm (having as a member a orney or agent) and the names of up to patent attorneys or agents. If no name is			
PLEASE NOTE: Unl recordation as set forti (A) NAME OF ASSIG	less an assignee is ident h in 37 CFR 3.11. Comp GNEE	ified below, no assignee oletion of this form is NO	T a substitute for filing an (B) RESIDENCE: (CIT)	natent. If an assign assignment. A and STATE OR C	COUNT	ΓRY)	ocument has been filed for
Please check the appropri	iate assignee category or	categories (will not be pr	rinted on the patent):	Individual 🖵 C	orporat	ion or other private gro	oup entity Government
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5. Change in Entity Stat	tus (from status indicates s SMALL ENT1TY state		b. Applicant is no lon				
	d Publication Fee (if req	uired) will not be accepte	d from anyone other than				e assignee or other party in
Authorized Signature				Date			
Typed or printed name							
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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

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COHEN, PONTA	ANI, LIEBERMAN &	SONG, MATTHEW J		
551 FIFTH AVEN	UE		ART UNIT	PAPER NUMBER
SUITE 1210 NEW YORK, NY 10176			1714 DATE MAILED; 10/28/201	0

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 772 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 772 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)	
As a second second	10/566,521	HARLE, VOLKER	
Notice of Allowability	Examiner	Art Unit	
	MATTHEW J. SONG	1714	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included will be mailed in due co	urse. THIS
1. \boxtimes This communication is responsive to <u>the amendment filed</u>	<u>8/11/2010</u> .		
2. The allowed claim(s) is/are <u>1-17</u> .			
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application No cuments have been received in this i	national stage application	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requi	rements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			ICE OF
 5. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the deponsion of the deponsion of	son's Patent Drawing Review (PTO- s Amendment / Comment or in the C .84(c)) should be written on the drawin he header according to 37 CFR 1.121(c sit of BIOLOGICAL MATERIAL n	office action of ngs in the front (not the ba a). nust be submitted. Not	
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO/SB/08),	5. ☐ Notice of Informal P. 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e nent/Comment	ance
U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06)	otice of Allowability	Part of Paper No./Mail	Date 20101024

Application/Control Number: 10/566,521 Page 2

Art Unit: 1714

EXAMINER'S AMENDMENT

Election/Restrictions

1. This application is in condition for allowance except for the presence of claim 18 directed to invention non-elected without traverse. Accordingly, claim 18 been cancelled.

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Cancel claim 18.

REASONS FOR ALLOWANCE

3. The following is an examiner's statement of reasons for allowance: The closest prior art is Haerle (US 6,100,104), Juergensen et al (WO 03054939 from IDS), where US 2005/0022725 is used as an accurate translation, and Tanaka et al (US 2001/0038655)

Harle teaches a method for production of a plurality of optoelectronic semiconductor chips **100** each having a plurality of structural elements **21**, **22**, **23** with respective at least one semiconductor layer (Figs 5-6 and col 7, ln 1-67), the method comprising the steps of providing a chip composite base having a substrate **3** and a growth surface (col 6, ln 30-67 and Fig 1-3); growing a mask material layer **4** onto the growth surface in such a way that mask material layer has a plurality of windows and/or opening **10** (col 6, ln 30-67 and Fig 3), a mask material being

Art Unit: 1714

chosen in such a way that a semiconductor material of the semiconductor layer that is to be grown in a later method step essentially cannot grow on the mask material or can grow in a substantially worse manner in comparison with the growth surface (col 6, ln 30-67 teaches SiO₂) or SiN and selective epitaxy such that monocrystalline growth on no mask portion and only polycrystalline growth, if any, on the mask); essentially simultaneously growing semiconductor layers on regions of the growth surface that lie within the windows (col 6, ln 30-67 teaches selective epitaxy within windows, thus clearly suggests simultaneously growing layers within the windows); and singulating the chip composite base with applied material to form semiconductor chips and a plural structural elements comprising a semiconductor layer sequence (col 7, ln 15-65 and Figs 5-6). Harle et al does not teach, suggest or provide any rationale for growing a nonclosed mask layer that has a plurality of statistically distributed windows with varying forms and/or openings because Harle et al teaches forming a closed mask layer which is patterned to have opening. A person of ordinary skill in the art would not grow a non-closed mask layer and form semiconductor device layers within the windows because the formation of devices requires precise control over masking and etching to produce identical and functioning devices.

Juergensen et al teaches a substrate with a growth surface ('725 [0002]); growing a nonclosed mask material layer (SiN or SiO) onto the growth surface in such a way that the mask material has a plurality of windows having varying forms and/or opening areas and a mask material being selected so that semiconductor layer that is to be grown essentially cannot grow on the mask material or can grow in a substantially worse manner in comparison to the growth surface ('725 [0005]). Juergensen et al does not teach, suggest or provide any rationale for growing semiconductor layers on regions of the growth surface that lie within the windows and Application/Control Number: 10/566,521 Page 4

Art Unit: 1714

forming semiconductor chips each having the structural elements comprising a semiconductor layer sequence because Juergensen et al teaches growing a layer within the windows to grow and coalesce to produce a continuous layer for subsequent growth. A person of ordinary skill in the art would not grow a non-closed mask layer and form semiconductor device layers within the windows because the formation of devices requires precise control over masking and etching to produce identical and functioning devices.

Tanaka et al teaches windows of a mask material comprise a plurality of statistically distributed windows having varying forms and opening areas (Fig 9A and [0128]-[0130]).

Tanaka et al teaches the windows are forming by etching and lithography. Tanaka et al does not teach, suggest or provide any rationale for growing a non-closed mask material layer having a plurality of statistically distributed windows having varying forms and opening areas.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to MATTHEW J. SONG whose telephone number is (571)272-1468. The examiner can normally be reached on M-F 11:00-7:00.

Application/Control Number: 10/566,521 Page 5

Art Unit: 1714

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Kornakov can be reached on 571-272-1303. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Matthew J Song Examiner Art Unit 1714

/Matthew J Song/ Examiner, Art Unit 1714